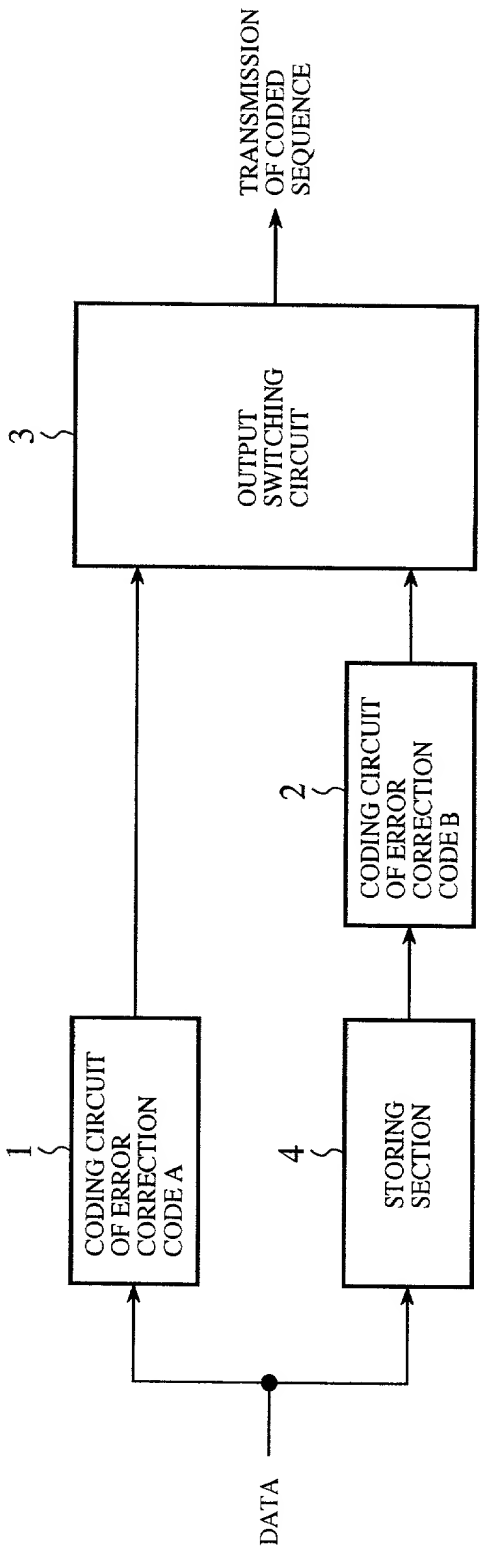
FIG.1


DATA
1
CODING CIRCUIT OF ERROR CORRECTION CODE A
2
CODING CIRCUIT OF ERROR CORRECTION CODE B
3
OUTPUT SWITCHING CIRCUIT
4
STORING SECTION
TRANSMISSION OF CODED SEQUENCE

EP 0 952 587 A2

FIG.2

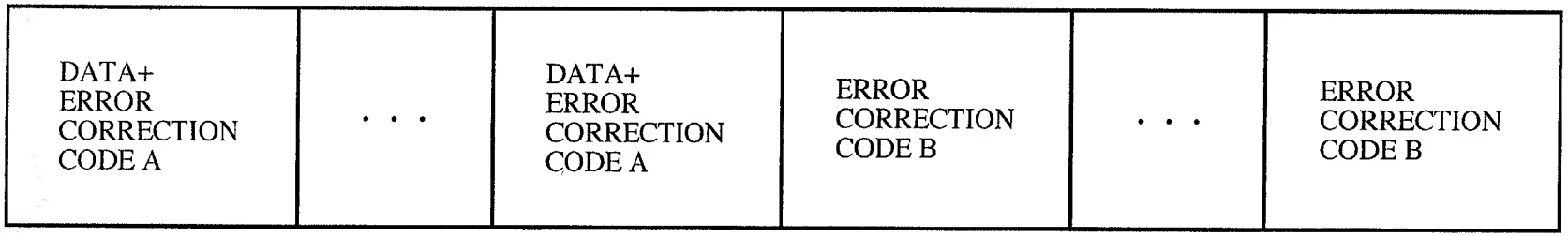


FIG.3

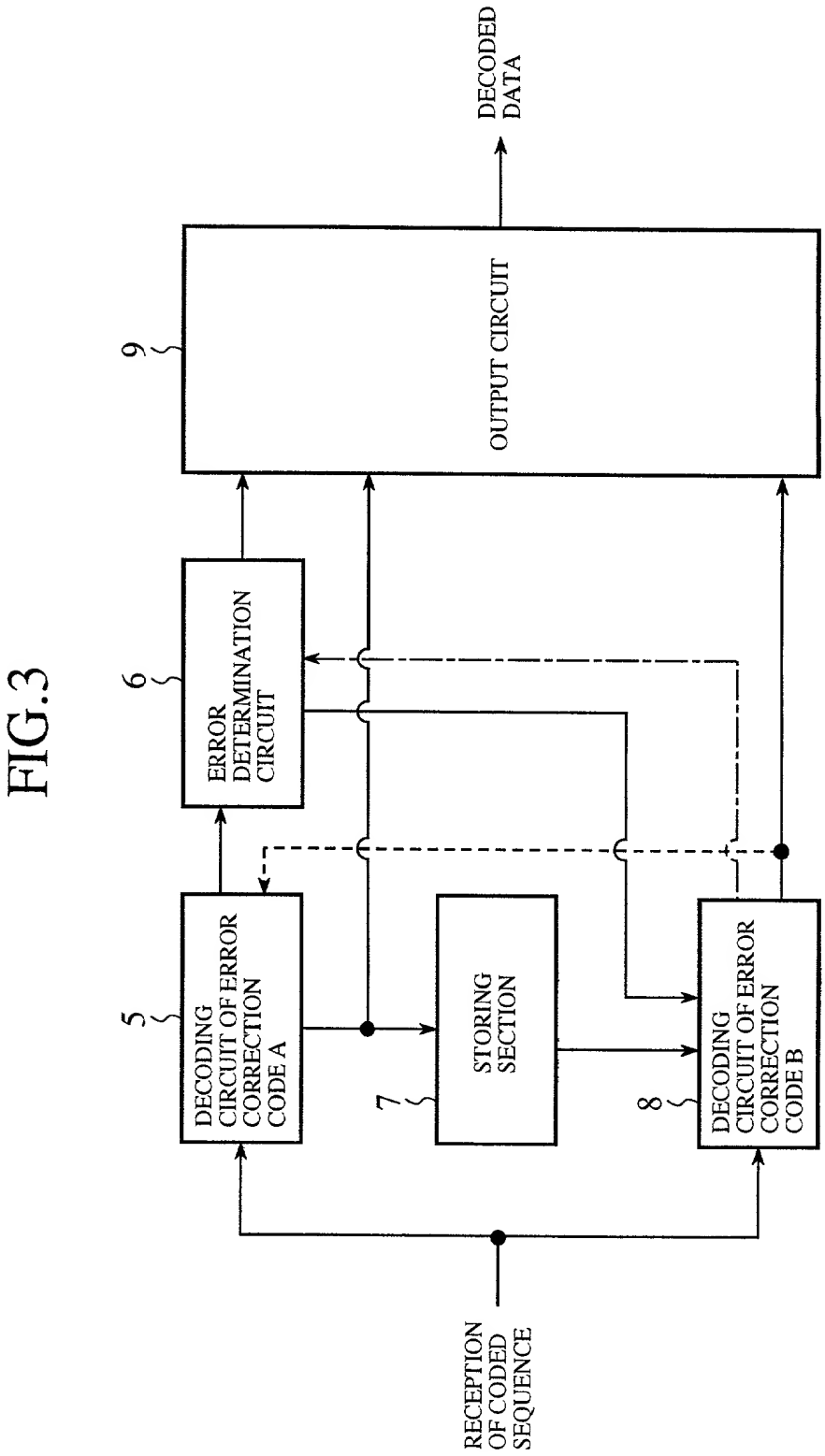

RECEPTION OF CODED SEQUENCE
DECODING CIRCUIT OF ERROR CORRECTION CODE A
5
STORING SECTION
7
DECODING CIRCUIT OF ERROR CORRECTION CODE B
8
ERROR DETERMINATION CIRCUIT
6
OUTPUT CIRCUIT
9
DECODED DATA

FIG.4

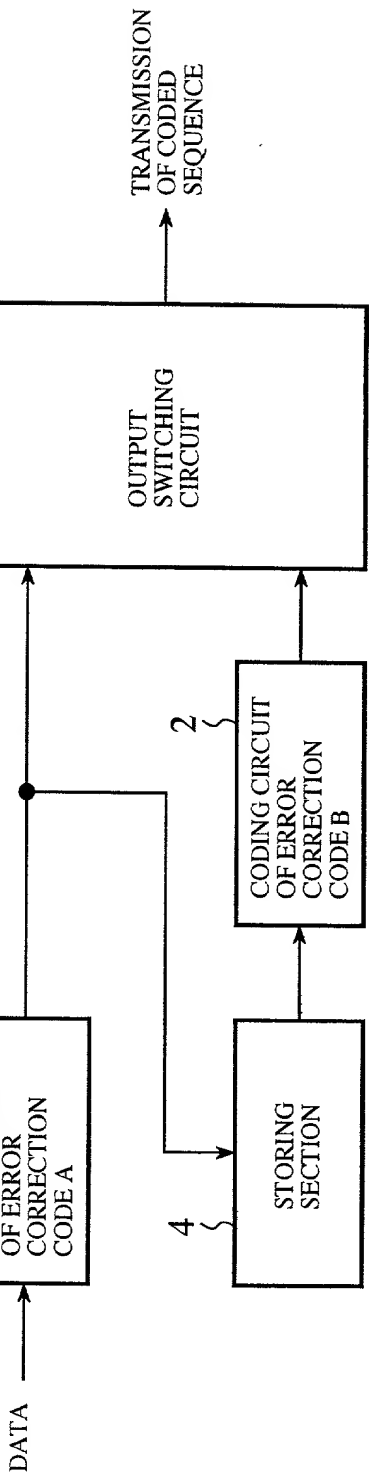

DATA
1
CODING CIRCUIT OF ERROR CORRECTION CODE A
2
CODING CIRCUIT OF ERROR CORRECTION CODE B
3
OUTPUT SWITCHING CIRCUIT
4
STORING SECTION
TRANSMISSION OF CODED SEQUENCE

FIG.5

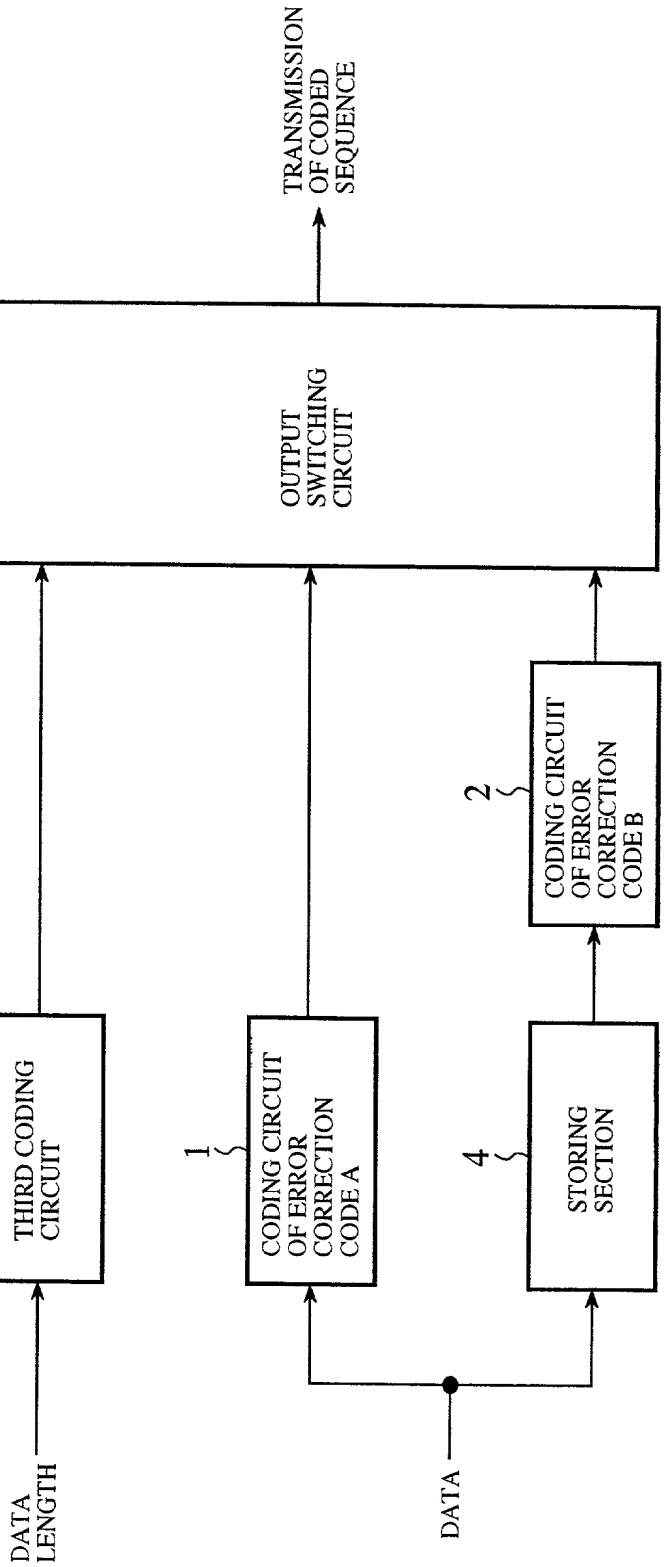

TRANSMISSION OF CODED SEQUENCE
3
OUTPUT SWITCHING CIRCUIT
10
THIRD CODING CIRCUIT
1
CODING CIRCUIT OF ERROR CORRECTION CODE A
2
CODING CIRCUIT OF ERROR CORRECTION CODE B
4
STORING SECTION
DATA LENGTH
DATA

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FIG.6

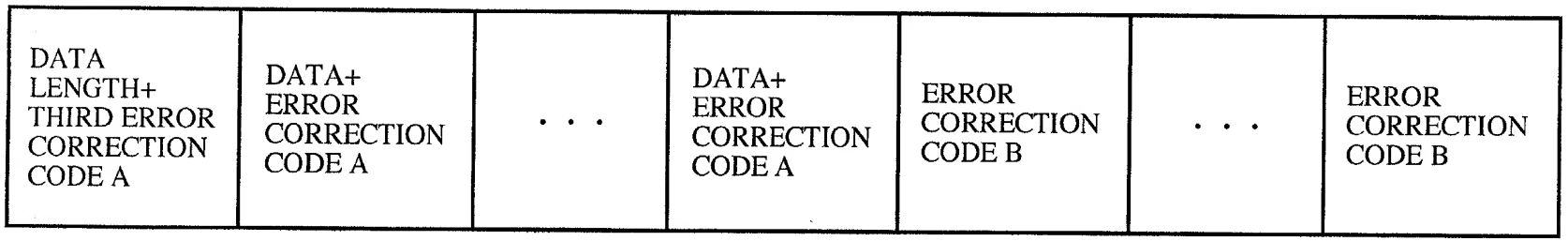
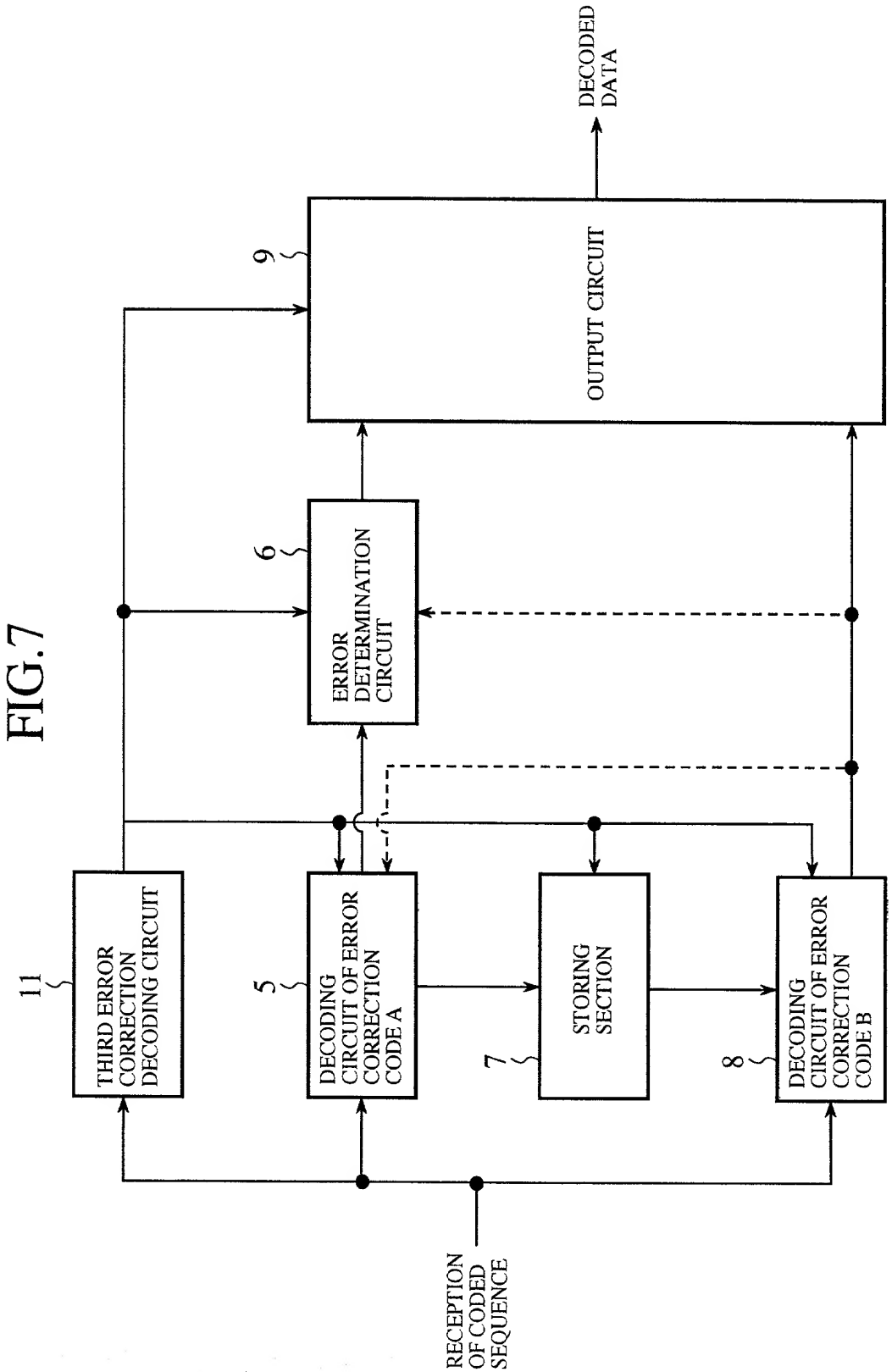


FIG.7


RECEPTION OF CODED SEQUENCE
11
THIRD ERROR CORRECTION DECODING CIRCUIT
5
DECODING CIRCUIT OF ERROR CORRECTION CODE A
7
STORING SECTION
8
DECODING CIRCUIT OF ERROR CORRECTION CODE B
6
ERROR DETERMINATION CIRCUIT
9
OUTPUT CIRCUIT
DECODED DATA

TOSZO"9EZTT660

FIG.8

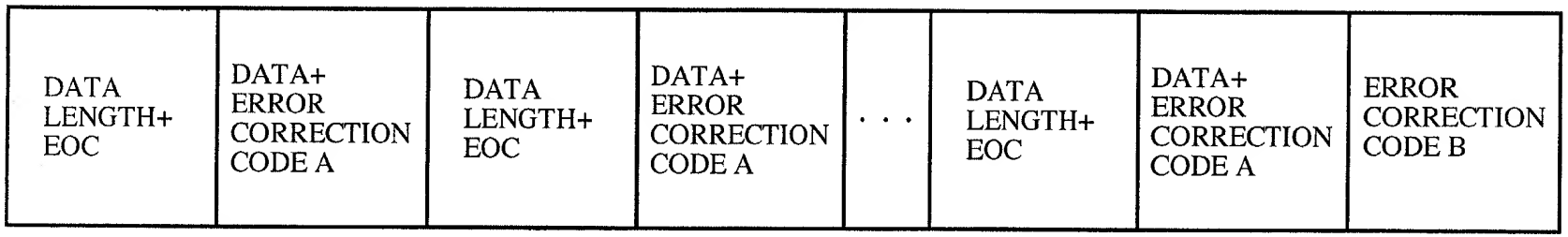
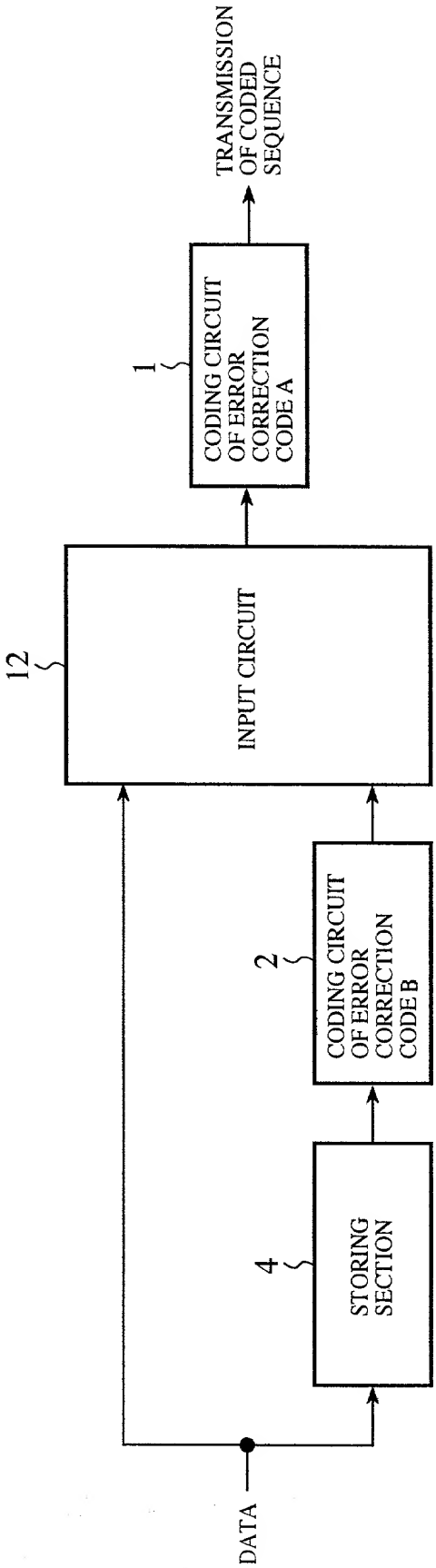


FIG.9


DATA
4
STORING SECTION
2
CODING CIRCUIT OF ERROR CORRECTION CODE B
12
INPUT CIRCUIT
1
CODING CIRCUIT OF ERROR CORRECTION CODE A
TRANSMISSION OF CODED SEQUENCE

EP 0 952 580 A2

FIG.10

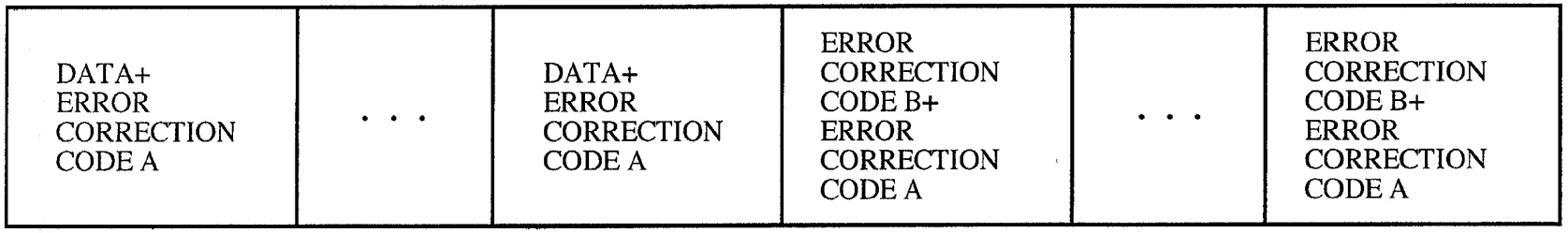


FIG.11

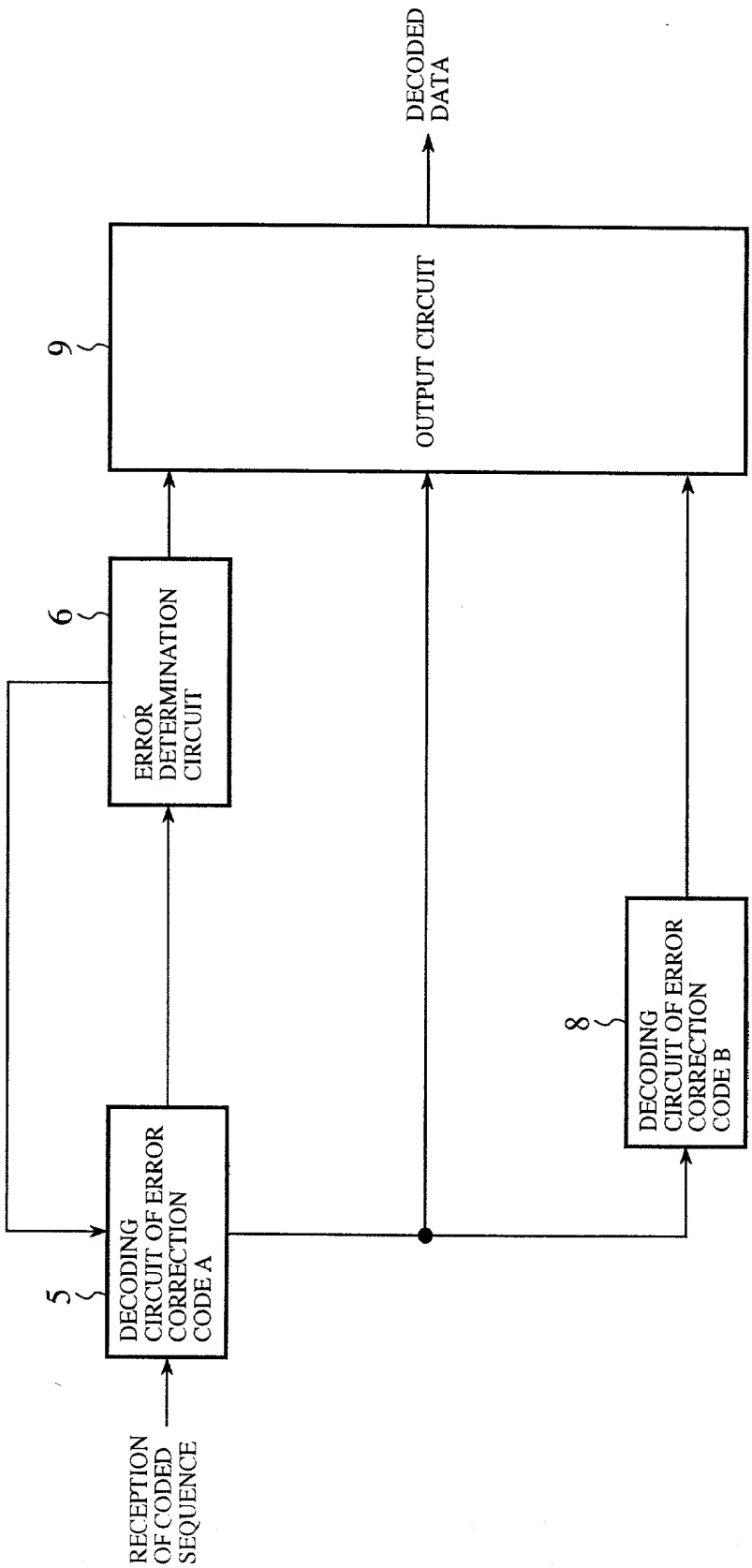

RECEPTION OF CODED SEQUENCE
5
DECODING CIRCUIT OF ERROR CORRECTION CODE A
6
ERROR DETERMINATION CIRCUIT
8
DECODING CIRCUIT OF ERROR CORRECTION CODE B
9
OUTPUT CIRCUIT
DECODED DATA

FIG.12

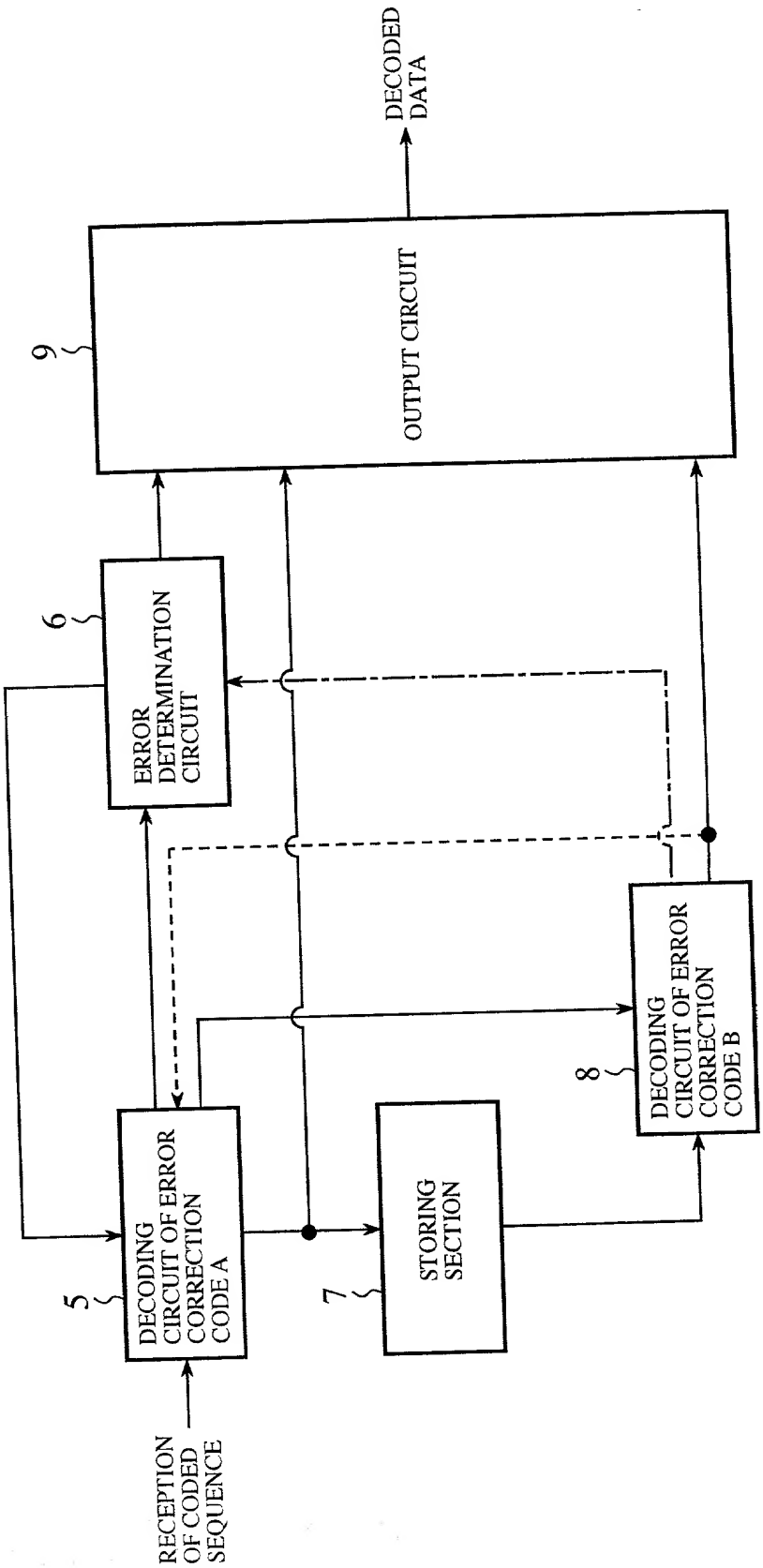

RECEPTION OF CODED SEQUENCE
5
DECODING CIRCUIT OF ERROR CORRECTION CODE A
7
STORING SECTION
8
DECODING CIRCUIT OF ERROR CORRECTION CODE B
6
ERROR DETERMINATION CIRCUIT
9
OUTPUT CIRCUIT
DECODED DATA

FIG.13

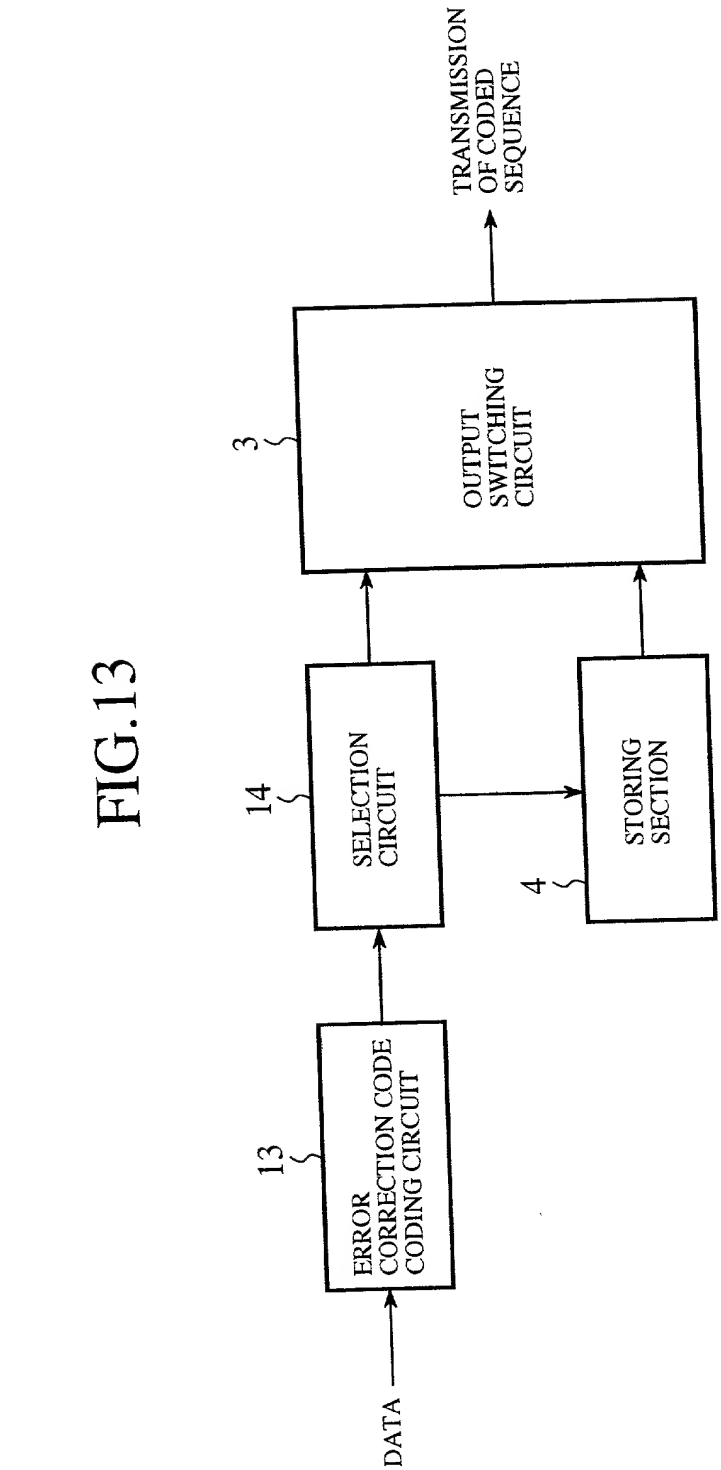

DATA
13
ERROR CORRECTION CODE CODING CIRCUIT
14
SELECTION CIRCUIT
4
STORING SECTION
3
OUTPUT SWITCHING CIRCUIT
TRANSMISSION OF CODED SEQUENCE

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FIG.14

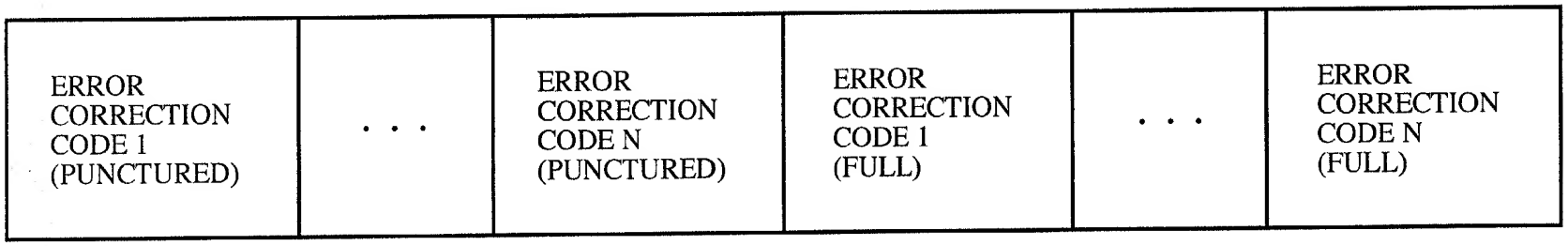

FIG.15

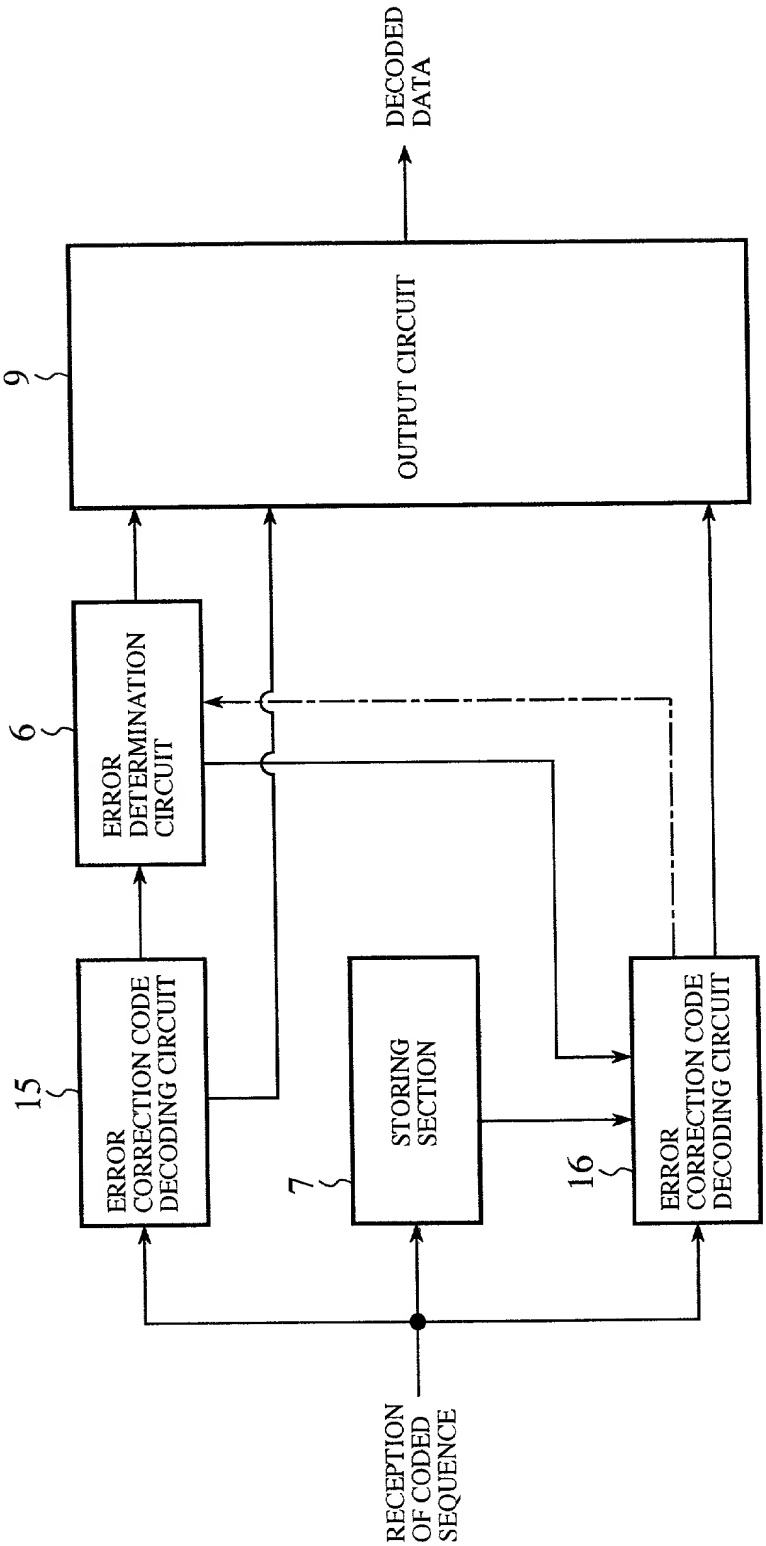
RECEPTION OF CODED SEQUENCE
ERROR CORRECTION CODE DECODING CIRCUIT
15
STORING SECTION
7
ERROR CORRECTION CODE DECODING CIRCUIT
16
ERROR DETERMINATION CIRCUIT
6
OUTPUT CIRCUIT
9
DECODED DATA

FIG.16

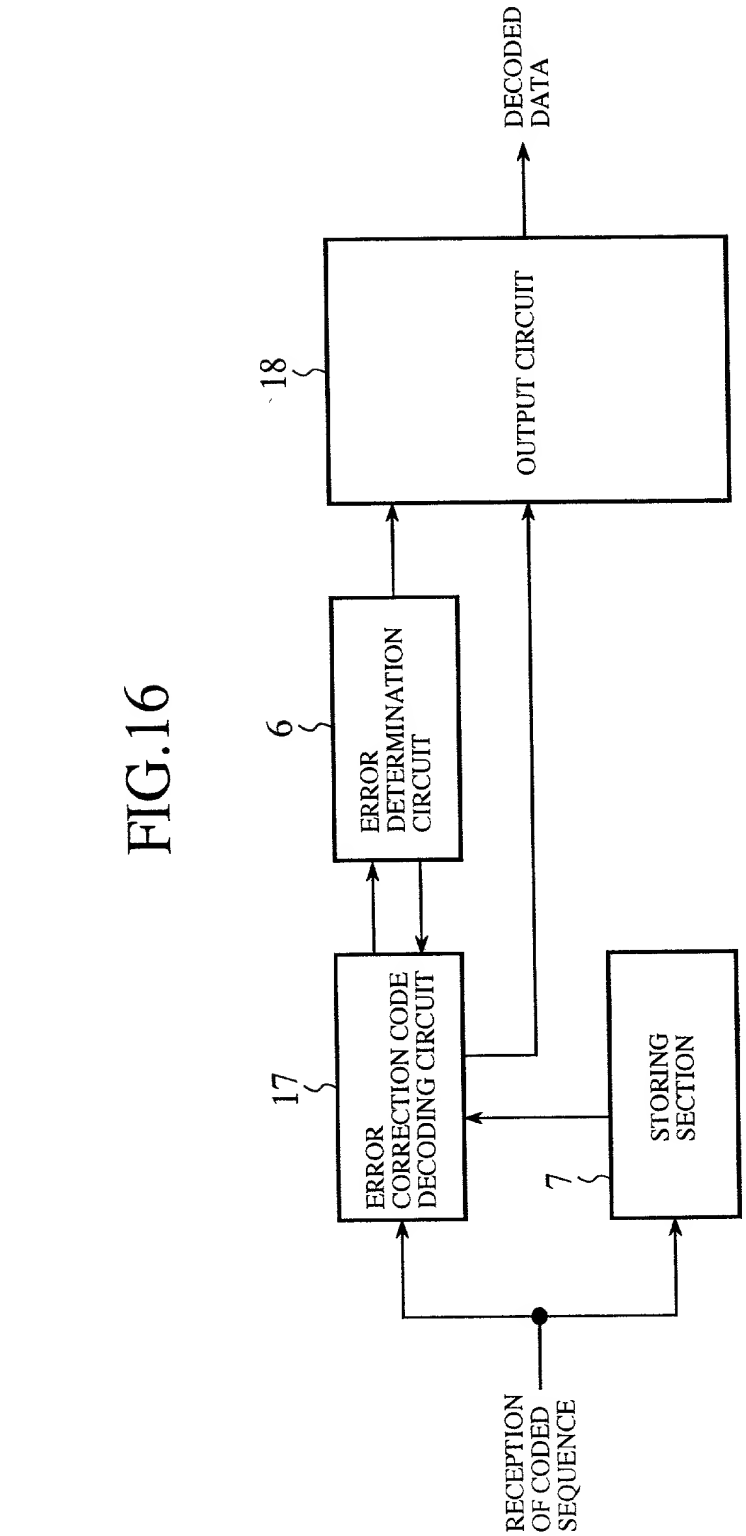
RECEPTION OF CODED SEQUENCE
ERROR CORRECTION CODE DECODING CIRCUIT
17
ERROR DETERMINATION CIRCUIT
6
STORING SECTION
7
OUTPUT CIRCUIT
18
DECODED DATA

FIG.17

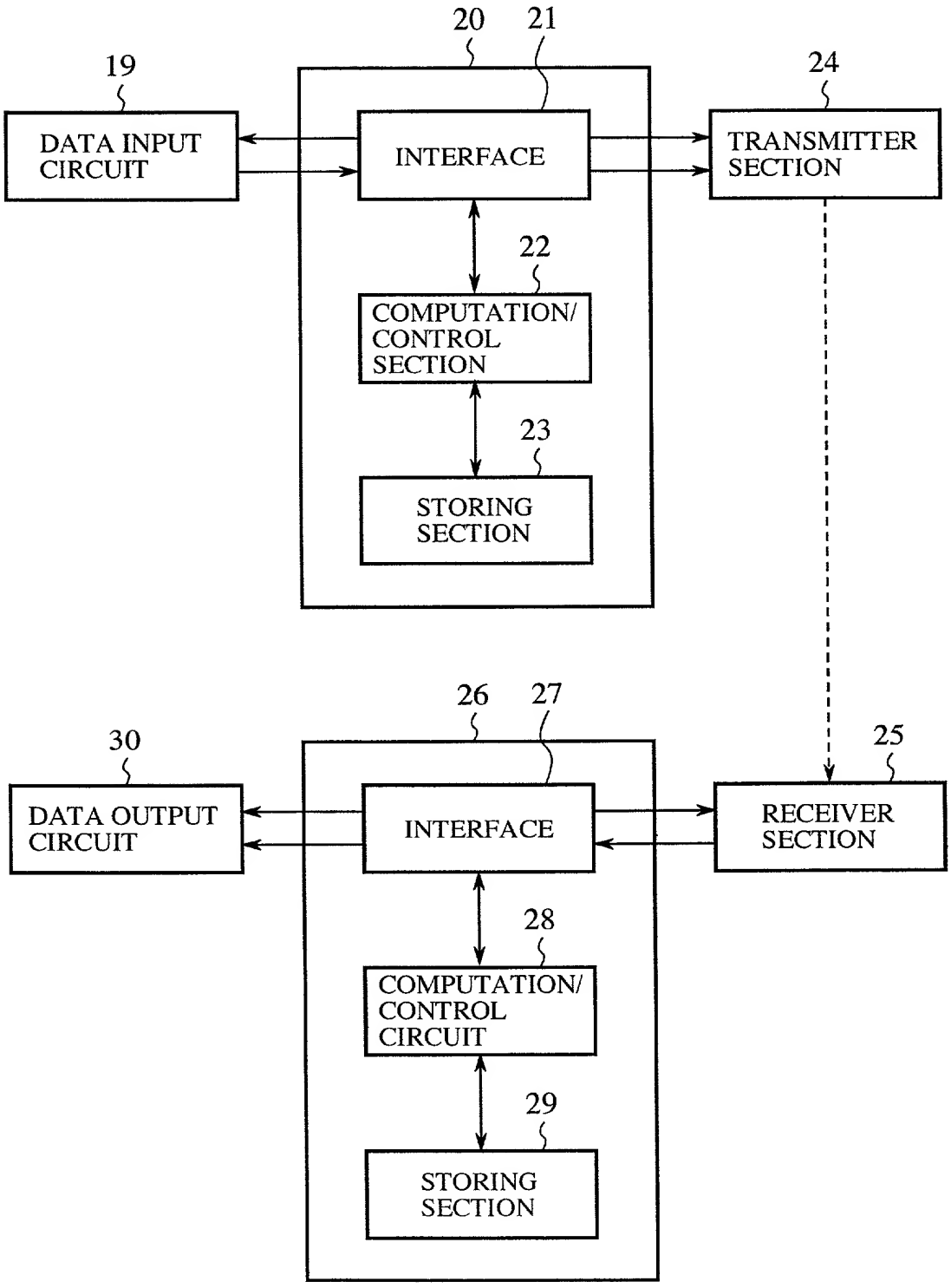
20
21
19
24
DATA INPUT CIRCUIT
INTERFACE
TRANSMITTER SECTION
22
COMPUTATION/ CONTROL SECTION
23
STORING SECTION
26
27
30
25
DATA OUTPUT CIRCUIT
INTERFACE
RECEIVER SECTION
28
COMPUTATION/ CONTROL CIRCUIT
29
STORING SECTION

FIG.18

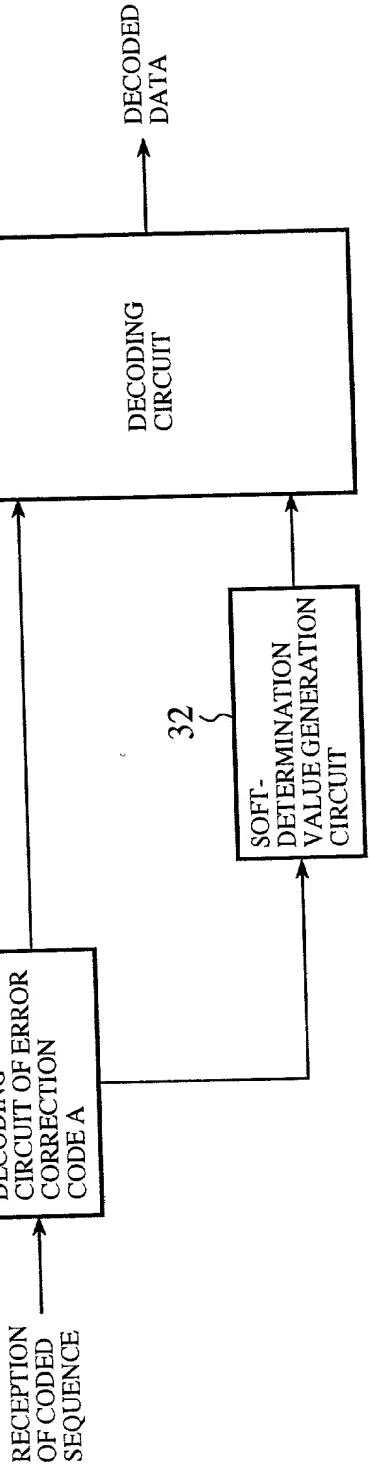
RECEPTION OF CODED SEQUENCE
31
DECODING CIRCUIT OF ERROR CORRECTION CODE A
32
SOFT-DETERMINATION VALUE GENERATION CIRCUIT
33
DECODING CIRCUIT
DECODED DATA

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FIG.19
(PRIOR ART)

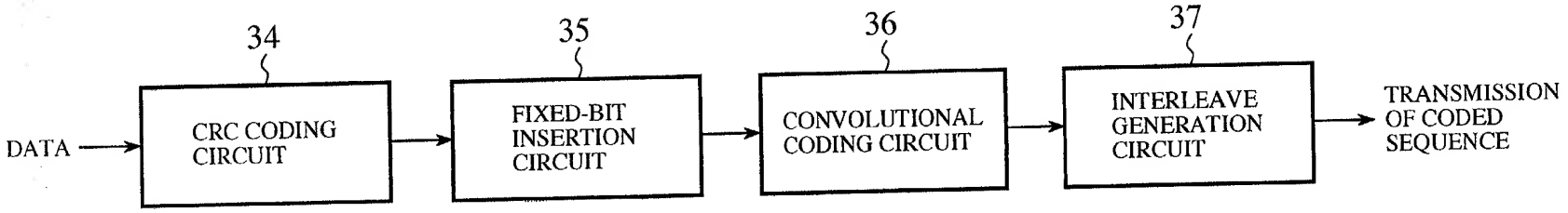

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FIG.20
(PRIOR ART)

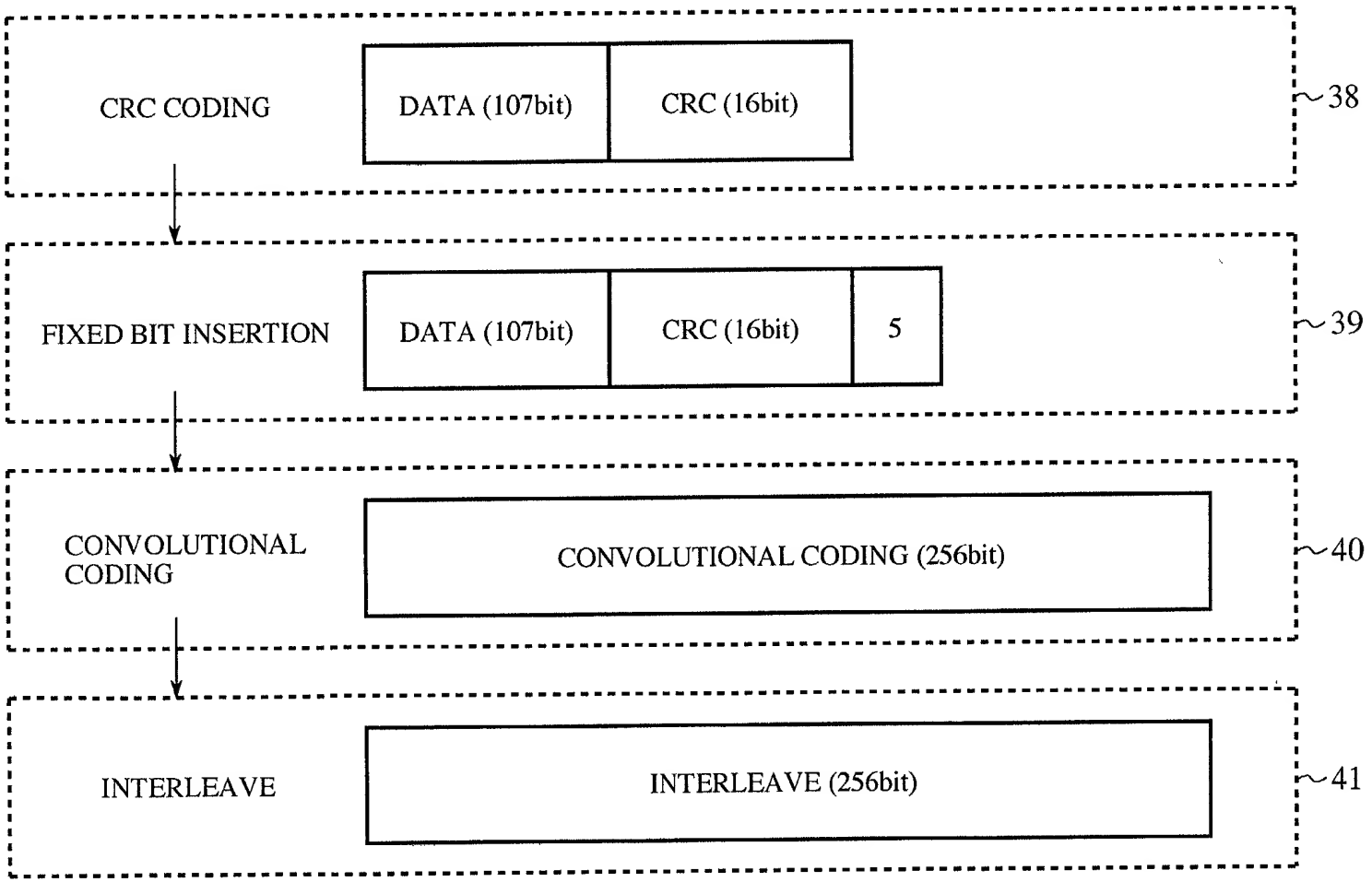

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FIG.21
(PRIOR ART)

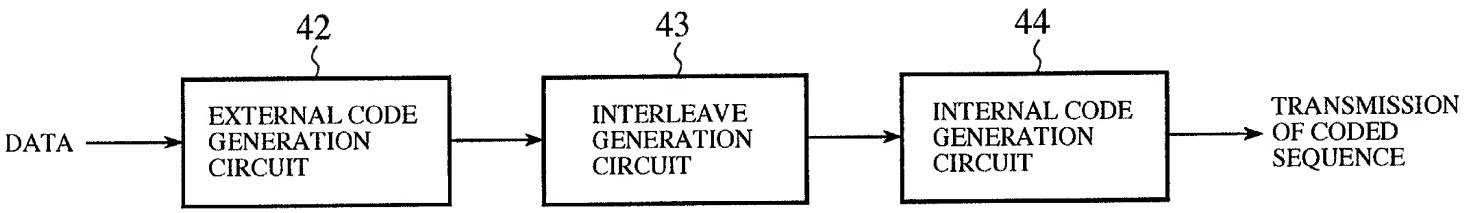
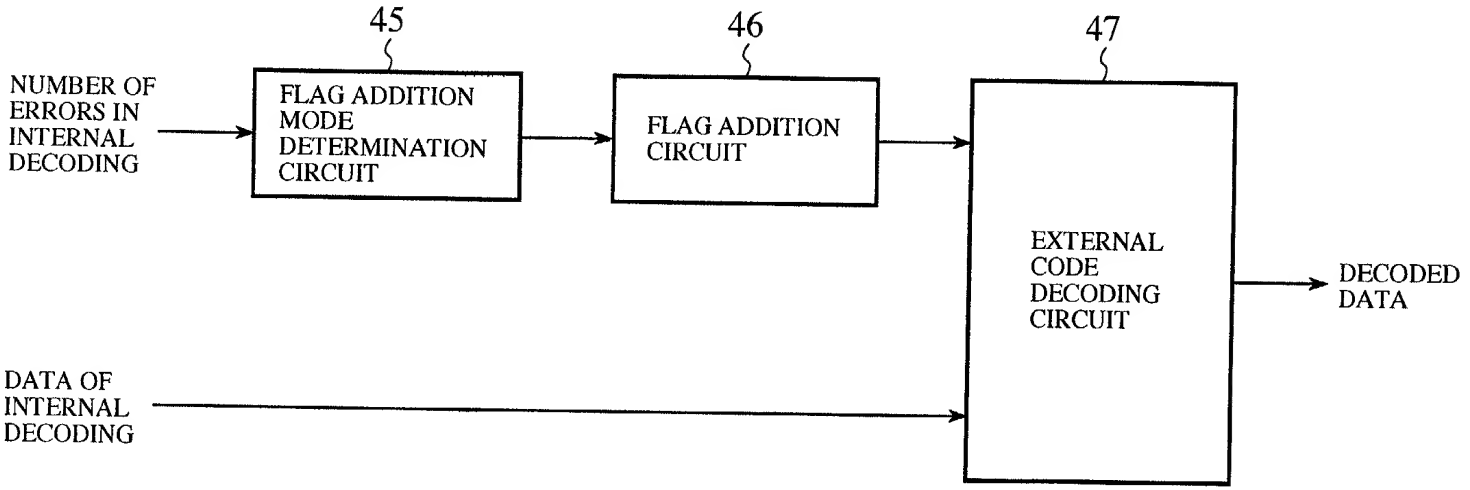
FIG.22
(PRIOR ART)